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ATTORNEY DOCKET 064747.1012

PATENT APPLICATION 10/824,874

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

n re Application of:

James D. Ballew et al.

Serial No.:

10/824,874

Filing Date:

April 15, 2004

Title:

High Performance Computing System and Method

Mail Stop: Amendments
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Date: 3-8-2006

INFORMATION DISCLOSURE STATEMENT

Applicants respectfully request, under 37 C.F.R. §§ 1.56, 1.97 and 1.98, that the references listed on the attached PTO-1449 form be considered and cited in the examination of the above-identified patent application. The present Application was filed after June 30, 2003. Therefore, under the July 11, 2003, waiver of 37 C.F.R. § 1.98(a)(2)(i) by the U.S. Patent and Trademark Office, copies of U.S. Patents and U.S. Patent Application Publications listed in the attached PTO-1449 form are not enclosed. All other references are enclosed for the convenience of the Examiner.

DAL01:896316.1

2

REMARKS

Each item of information contained in this Information Disclosure Statement was first cited in a communication from a foreign patent office in a counterpart foreign application not more than thirty days prior to the filing of this Information Disclosure Statement, and this Information Disclosure Statement is submitted before a first Office Action on the merits. Under 37 C.F.R. § 1.97(b)(3), no fee is due. Nonetheless, the Commissioner is hereby authorized to charge any fee and credit any overpayment to Deposit Account No. 02-0384 of Baker Botts L.L.P.

Respectfully submitted,

BAKER BOTTS L.L.P. Attorneys for Applicants

Travis W. Thomas Reg. No. 48,667

Date: 3-8-2006

Correspondence Address:

Customer Number 45507

Information Disclosure Citation In an Application					Application No. 10/824,874	Applicant(s) James D. Ballew et al.					
					Docket Number 064747.1012	Group Art			ling Date oril 15, 2004		
		8 2006		U.S.	PATENT DOCUM	ENTS					
Q.	_	DOC MENT NO.	DATE		NAME	E CLASS		SUBCLASS		FILING DATE	
	A D	DOC MENT NO. 3,301,104	04/05/1994	Yala	manchili	395	395 800		08/07/1	08/07/1990	
	В										
FOREIGN PATENT DOCUMENTS											
		DOCUMENT NO. DATE			COUNTRY	CLASS	SUBCLASS		TRANSLATION		
	С		!						YES	NO	
	D									·	
				NON	-PATENT DOCUM	IENTS					
		DOCUMENT (Including Author, Title, Source, and Pertinent Pages)							DATE		
	E	Communication from the European Patent Office, European Search Report for Application No. PCT/US2005/012314, 6 pages, 09/20/2005, and Written Opinion of the International Search Authority, 7 pages							Mailed 02/10/2006		
	F	Feitelson, "Job Scheduling in Multiprogrammed Parallel Systems", IBM Research Report, August 1997, pages 1-4, XP002942107								August 1997	
	G	Kandlur et al., "Hypercube Management in the Presence of Node Failures", third conference on hypercube concurrent computers and applications ACM New York, 1988, pages 328-336, vol. 1, XP002364194								1998	
	Н	Wenjian et al., "Efficient Processor Allocation for 3D Tori", Parallel Processing Symposium 1995, Proceedings, 9th International, April 25-28, 1995, IEEE Comput. Soc., pages 466-471, XP010135921								1995	
	I Ma et al., "E-Kernel: An Embedding Kernel on the IBM Victor V256, Multiprocessor for Program Mapping and Network Reconfiguration, IEEE Transactions on Parallel and Distribute Systems, IEEE Service Center, vol. 5, no. 9, September 1, 1994, pages 977-994, XP000460490							[September 1, 1994		
EX	AMΠ		·		DATE CONSIDERED						
		ER: Initial if citation consider . Include copy of this form w				EP § 609. Draw line	through citation	if not	in conformance	e and not	

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